The Multiple-Valued Logic Technical Committee of the IEEE Computer Society will hold its 48th annual symposium in Linz, Austria, on May 16th-18th, 2018. You are invited to submit original papers, surveys, or tutorial papers on any subject in the area of multiple-valued logic, including but not limited to:

- Algebra and Formal Aspects
- Automatic Reasoning
- Logic Programming
- Philosophical Aspects of MVL
- Fuzzy Logic and Soft Computing
- Data Mining
- Machine Learning and Robotics
- Quantum Computing
- Reversible Computing
- Logic Design and Switching Theory
- Test and Verification
- Spectral Techniques
- Circuit/Device Implementation
- VLSI Architecture and Computing
- System-on-Chip Technology
- MVL Aspects of Nanotechnology
- MVL Aspects of Medical Technologies
- MVL Approaches to Big Data

Authors should submit papers as PDF files following the IEEE style guidelines for conference proceedings. Each manuscript shall include a 50-100 word abstract and shall not exceed 6 pages. All accepted papers will be published by IEEE. The authors of selected papers will be invited after the conference to prepare an extended version of their paper to be published in the journal of Multiple-Valued Logic and Soft Computing.

In conjunction with the conference, the International Workshop on Post-Binary ULSI Systems (ULSIWS) will be held on May 15th, 2018. A separate call will follow.

**Important Dates:**
- Abstract Deadline: November 19th, 2017 (extended)
- Paper Deadline: November 26th, 2017 (extended)
- Author Notification: February 4th, 2018
- Camera-ready Version: March 4th, 2018
- Conference: May 16th-18th, 2018
- ULSIWS-Workshop: May 15th, 2018

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